

Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1. (Original) A current drive circuit which supplies a signal current to a node of a driven circuit through a signal line,

wherein a precharge means for supplying a precharge voltage to the node through the signal line is provided; and

the precharge means has a supply means for supplying the precharge voltage to the node and the signal line prior to supplying the signal current.

2. (Original) The current drive circuit according to claim 1,

wherein the precharge means comprises a setting means for setting the precharge voltage to a potential equal to or according to a potential of the node in a stationary state when the signal current is supplied to the driven circuit.

3. (Original) The current drive circuit according to claim 1,

wherein the precharge means comprises a plural setting means for setting a plurality of the precharge voltages; and

a selection supply means for selectively supplying the precharge voltage to the node and the signal line according to a magnitude of the signal current.

4. (Original) A current drive circuit which supplies a signal current to a node of a driven circuit through a signal line, comprising:

a precharge circuit for supplying a precharge voltage to the node and the signal line;

a generation means for supplying the signal current to the precharge circuit and generating the precharge voltage; and

a supply means for supplying the precharge voltage to the node and the signal line prior to supplying the signal current to the driven circuit.

5. (Original) A current drive circuit which supplies a signal current to a node of a driven circuit through a signal line, comprising:

a precharge circuit for supplying a precharge voltage to the node; and

a supply means for generating the precharge voltage in advance by supplying a current corresponding to the signal current to the precharge circuit and then supplying the precharge voltage to the node and the signal line prior to supplying the signal current to the driven circuit.

6. (Currently Amended) The current drive circuit according to ~~claims 4 or 5~~ claim 4, wherein the driven circuit comprises a first driving element and the precharge circuit comprises a second driving element; and

the first and the second driving elements are the same in size or in size according to it.

7. (Currently Amended) The current drive circuit according to ~~claims 4 or 5~~ claim 4, comprising:

a means for supplying the precharge voltage to the node and the signal line through an impedance transformation amplifier.

8. (Currently Amended) The current drive circuit according to ~~claims 4 or 5~~ claim 4, comprising:

a plural setting means for setting a plurality of the precharge voltages; and

a selection supply means for selectively supplying the precharge voltage to the node and the signal line according to a magnitude of the signal current.

9. (Currently Amended) The current drive circuit according to ~~any one of claims 1, 4, or 5~~ claim 1, comprising:

a means for setting a precharge period T_b for supplying the precharge voltage to the node and the signal line so as to satisfy

$$T_b = R_L \times C_L$$

based on a wiring resistance R_L and a parasitic capacitance C_L of the signal line.

10. (Original) The current drive circuit according to claim 9, comprising:

a means for setting as

$$T_a = T_b$$

in the case where a supply period T_a of the signal current to the driven circuit satisfies
 $T_a < T_b$.

11. (Original) A display device comprising:

an image circuit to which an image data is provided as a current through a current line;
and

a current drive circuit for supplying the image data to the current line as a signal current,

wherein the current drive circuit comprising:

a source driver current source for supplying a signal current according to the image data from a node to the current line;

a precharge circuit for supplying a precharge voltage to the node and the current line;
and

a supply means for supplying the precharge voltage to the node and the current line prior to supplying the signal current.

12. (Original) The display device according to claim 11, comprising:
a means for supplying the precharge voltage to the current line through an impedance transformation amplifier.

13. (Original) A display device comprising:
a pixel circuit comprising a signal line for transmitting image data as a signal current and a first driving element for supplying a driving current in proportion to the signal current from a power supply line; and
a source driver circuit comprising an image signal input current source for supplying the signal current to the signal line,
wherein a precharge circuit for precharging the signal line prior to supplying the signal current to the signal line is integrated in the source driver circuit.

14. (Original) The display device according to claim 13,
wherein the precharge circuit is selectively connected between the image signal input current source and the power supply line and comprises a second driving element for outputting a precharge voltage according to the signal current.

15. (Currently Amended) The display device according to ~~claims 13 or 14~~ claim 13, comprising:
a means for supplying the precharge voltage to the signal line through an impedance transformation amplifier.

16. (Original) A display device comprising:

a pixel circuit comprising a signal line for transmitting image data as a signal current and a first driving element for supplying a driving current in proportion to the signal current from a power supply line; and

a source driver circuit comprising an image signal input current source for supplying the signal current to the signal line,

wherein a precharge circuit for precharging the signal line prior to supplying the signal current to the signal line is integrated in the source driver circuit;

the precharge circuit is selectively connected between the image signal input current source and the power supply line and comprises a second driving element for outputting a precharge voltage according to the signal current; and

the first and the second driving elements are the same in size or in size according to it.

17. (Currently Amended) The display device according to ~~claims 13 or 16~~ claim 13, comprising:

a means for setting the precharge voltage to a value equal to or according to a voltage in a stationary state when the signal current is supplied to the first or the second driving element.

18. (Original) A current drive circuit comprising:

a driven circuit;

a precharge circuit;

a means for controlling a connection between a driven circuit and a precharge circuit; and

a means for controlling a connection between a driven circuit and a current source circuit.

19. (Original) A current drive circuit comprising:

- a driven circuit;
- a precharge circuit;
- a current source circuit for inputting a signal current to the driven circuit;
- a means for controlling a connection between the driven circuit and the precharge circuit;
- a means for controlling a connection between the driven circuit and the current source circuit; and
- a means for outputting a voltage by amplifying a signal outputted from the precharge circuit.

20. (Original) A current drive circuit comprising:

- a driven circuit;
- a precharge circuit;
- a current source circuit for inputting a signal current to the driven circuit;
- a means for controlling a connection between the driven circuit and the precharge circuit;
- a means for controlling a connection between the driven circuit and the current source circuit, wherein
 - the precharge circuit outputs a precharge voltage selected from a plurality of voltage values.

21. (Original) A current drive circuit comprising:

- a driven circuit;
- a precharge circuit;
- a current source circuit for inputting a signal current to the driven circuit;

a means for controlling a connection between the driven circuit and the precharge circuit;

a means for controlling a connection between the driven circuit and the current source circuit; and a means for amplifying a signal outputted from the precharge circuit,

wherein the precharge circuit outputs a precharge voltage selected from a plurality of voltage values.

22. (Currently Amended) The current drive circuit according to ~~any one of claims 18 to 21~~ claim 18,

wherein the driven circuit comprises a first transistor and the precharge circuit comprises a second transistor.

23. (Original) The current drive circuit according to claim 22,

wherein a proportion of a channel width W and a channel length L of the first transistor and a proportion of a channel width W and a channel length L of the second transistor are almost equal to each other.

24. (Currently Amended) The current drive circuit according to ~~any one of claims 18 to 21~~ claim 18, comprising:

a means for setting a precharge period T_b for supplying the precharge voltage to the wiring, a resistance R_L of the wiring, and a parasitic capacitance C_L so as to satisfy

$$T_b = R_L \times C_L$$

25. (Currently Amended) The current drive circuit according to ~~any one of claims 18 to 21~~ claim 18, comprising:

a means for driving the driven circuit with a binary signal voltage.

26. (Currently Amended) The current drive circuit according to ~~any one of claims 18 to 21~~ claim 18,

wherein the driven circuit is disposed in a pixel of a display device; and the precharge circuit and the current source circuit are disposed in a source driver circuit of the display device.

27. (Currently Amended) The current drive circuit according to ~~any one of claims 18 to 21~~ claim 18,

wherein the driven circuit is disposed in a digital voltage/analog current conversion circuit; and

the precharge circuit and the current source circuit are disposed in a reference current source circuit.

28. (Currently Amended) The current drive circuit according to ~~any one of claims 18 to 21~~ claim 18,

wherein the precharge circuit comprises a transistor; and a gate and a drain of the transistor are connected to each other.

29. (New) The current drive circuit according to claim 5,

wherein the driven circuit comprises a first driving element and the precharge circuit comprises a second driving element; and

the first and the second driving elements are the same in size or in size according to it.

30. (New) The current drive circuit according to claim 5, comprising:

a means for supplying the precharge voltage to the node and the signal line through an impedance transformation amplifier.

31. (New) The current drive circuit according to claim 5, comprising:
a plural setting means for setting a plurality of the precharge voltages; and
a selection supply means for selectively supplying the precharge voltage to the node and
the signal line according to a magnitude of the signal current.

32. (New) The current drive circuit according to claim 4, comprising:
a means for setting a precharge period T_b for supplying the precharge voltage to the
node and the signal line so as to satisfy

$$T_b = R_L \times C_L$$

based on a wiring resistance R_L and a parasitic capacitance C_L of the signal line.

33. (New) The current drive circuit according to claim 5, comprising:
a means for setting a precharge period T_b for supplying the precharge voltage to the
node and the signal line so as to satisfy

$$T_b = R_L \times C_L$$

based on a wiring resistance R_L and a parasitic capacitance C_L of the signal line.

34. (New) The current drive circuit according to claim 32, comprising:

a means for setting as

$$T_a = T_b$$

in the case where a supply period T_a of the signal current to the driven circuit satisfies
 $T_a < T_b$.

35. (New) The current drive circuit according to claim 33, comprising:

a means for setting as

$$T_a = T_b$$

in the case where a supply period T_a of the signal current to the driven circuit satisfies
 $T_a < T_b$.

36. (New) The display device according to claim 14, comprising:
a means for supplying the precharge voltage to the signal line through an impedance transformation amplifier.

37. (New) The display device according to claim 16, comprising:
a means for setting the precharge voltage to a value equal to or according to a voltage in a stationary state when the signal current is supplied to the first or the second driving element.

38. (New) The current drive circuit according to claim 19,
wherein the driven circuit comprises a first transistor and the precharge circuit comprises a second transistor.

39. (New) The current drive circuit according to claim 20,
wherein the driven circuit comprises a first transistor and the precharge circuit comprises a second transistor.

40. (New) The current drive circuit according to claim 21,
wherein the driven circuit comprises a first transistor and the precharge circuit comprises a second transistor.

41. (New) The current drive circuit according to claim 38,
wherein a proportion of a channel width W and a channel length L of the first transistor and a proportion of a channel width W and a channel length L of the second transistor are almost equal to each other.

42. (New) The current drive circuit according to claim 39,
wherein a proportion of a channel width W and a channel length L of the first transistor
and a proportion of a channel width W and a channel length L of the second transistor are
almost equal to each other.

43. (New) The current drive circuit according to claim 40,
wherein a proportion of a channel width W and a channel length L of the first transistor
and a proportion of a channel width W and a channel length L of the second transistor are
almost equal to each other.

44. (New) The current drive circuit according to claim 19, comprising:
a means for setting a precharge period T_b for supplying the precharge voltage to the
wiring, a resistance R_L of the wiring, and a parasitic capacitance C_L so as to satisfy

$$T_b = R_L \times C_L.$$

45. (New) The current drive circuit according to claim 20, comprising:
a means for setting a precharge period T_b for supplying the precharge voltage to the
wiring, a resistance R_L of the wiring, and a parasitic capacitance C_L so as to satisfy

$$T_b = R_L \times C_L.$$

46. (New) The current drive circuit according to claim 21, comprising:
a means for setting a precharge period T_b for supplying the precharge voltage to the
wiring, a resistance R_L of the wiring, and a parasitic capacitance C_L so as to satisfy

$$T_b = R_L \times C_L.$$

47. (New) The current drive circuit according to claim 19, comprising:
a means for driving the driven circuit with a binary signal voltage.

48. (New) The current drive circuit according to claim 20, comprising:
a means for driving the driven circuit with a binary signal voltage.

49. (New) The current drive circuit according to claim 21, comprising:
a means for driving the driven circuit with a binary signal voltage.

50. (New) The current drive circuit according to claim 19,
wherein the driven circuit is disposed in a pixel of a display device; and
the precharge circuit and the current source circuit are disposed in a source driver
circuit of the display device.

51. (New) The current drive circuit according to claim 20,
wherein the driven circuit is disposed in a pixel of a display device; and
the precharge circuit and the current source circuit are disposed in a source driver
circuit of the display device.

52. (New) The current drive circuit according to claim 21,
wherein the driven circuit is disposed in a pixel of a display device; and
the precharge circuit and the current source circuit are disposed in a source driver
circuit of the display device.

53. (New) The current drive circuit according to claim 19,
wherein the driven circuit is disposed in a digital voltage/analog current conversion
circuit; and
the precharge circuit and the current source circuit are disposed in a reference current
source circuit.

54. (New) The current drive circuit according to claim 20,
wherein the driven circuit is disposed in a digital voltage/analog current conversion
circuit; and

the precharge circuit and the current source circuit are disposed in a reference current
source circuit.

55. (New) The current drive circuit according to claim 21,
wherein the driven circuit is disposed in a digital voltage/analog current conversion
circuit; and

the precharge circuit and the current source circuit are disposed in a reference current
source circuit.

56. (New) The current drive circuit according to claim 19,
wherein the precharge circuit comprises a transistor; and
a gate and a drain of the transistor are connected to each other.

57. (New) The current drive circuit according to claim 20,
wherein the precharge circuit comprises a transistor; and
a gate and a drain of the transistor are connected to each other.

58. (New) The current drive circuit according to claim 21,
wherein the precharge circuit comprises a transistor; and
a gate and a drain of the transistor are connected to each other.

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Amendments to the Drawings:

Please substitute the attached drawings in English for the drawings submitted with the application.